

Development of Digital Low Level Radio Frequency Controller at SSRF

Digital low level radio frequency technology has been adopted in the storage ring of SSRF and a controller based on commercial FPGA and DSP board has been developed and operated successfully which helps SSRF to satisfy its specification with beam as high as 300mA. The performance improved controller has been fabricated in house and the stability of amplitude and phase reached 0.076% (RMS) and 0.054 degree (RMS) respectively during the test with 210mA. It is scheduled to be operated before the end of 2013. The recent progress on digital LLRF for FEL will be also reported such as the development activities and test results on the local oscillation generation board and down converter board.

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